

Electronic Acknowledgement Receipt

EFS ID:	3601237
Application Number:	10814968
International Application Number:	
Confirmation Number:	8696
Title of Invention:	INTEGRATED CIRCUIT HAVING A MEMORY CELL ARRAY CAPABLE OF SIMULTANEOUSLY PERFORMING A DATA READ OPERATION AND A DATA WRITE OPERATION
First Named Inventor/Applicant Name:	Kyo-Min Sohn
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Attorney Docket Number:	8021-225JHM/SS18860US
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	issue-fee-trans-8021-225.pdf	182352	no	1
			8cca01818b5a95a168b309e382ff52d9d9cb0a62		

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	8535	no	2
			5554f687abbaa666823016f25906c7280f263927		

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Total Files Size (in bytes):			190887
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